
Optisystem 9 0 Serial

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The basic idea of the DP-QPSK modulation format is that the data encoded by DP can be directly mapped into the bit stream of QPSK. Thus, it can carry over the QPSK transmission without modulation and demodulation so as to reduce the hardware and software complexity. At the same time, the information capacity of the system is greatly improved. A serial DP-QPSK format transmission system using the IC Photonics 125 Gbit/s DP-QPSK modulation format was built, a proof-of-concept system was verified and the test results are shown in the paper. the bit stream is demodulated by receiving iq demodulator. it converts the incoming i,q signals into in-phase (i) and quadrature (q) signals. the i,q signals in the two directions are then combined by the polarization combiner to form a single i,q signal with orthogonal polarization. this pair of i,q signals is then amplified by i,q amplifiers to a large-amplitude i,q signal. after going through an iq modulator and low-pass filter, the 2-bit signal is converted into two serial data symbols (fig. 2). a serial stream of data consists of a specific pattern of '1's and '0's, not just any sequence of ones and zeros. serial streams typically occupy time slots. a time slot allows for transmission of a single bit of data at a time (perhaps for synchronization purposes, or due to hardware limitations). the data is transmitted in a steady stream of zeros followed by a steady stream of ones. the data is transmitted in a specific sequence in time:

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this project file is to show a basic example of transmission layer design. optisystem has the ability to simulate and design all standard and non-standard optical components, such as sources, detectors, polarizers, isolators, etc. the definition of a serial data stream, also known as a serial data signal or a serial bit stream, is a data stream that contains a stream of bits. each bit in the data stream is represented by a voltage value, typically a high or low voltage value; the sequence of these values forms the stream. the data is sent in series and is usually synchronised. in general, one bit of data is transmitted at a time; the bit stream is divided into data segments, each of which is referred to as a byte. the number of bits in a byte is typically eight. an 8-bit byte is equivalent to a bit. each bit is represented by a voltage value, typically a high or low voltage value; the sequence of these values forms the stream. the optisystem simulation model enables you to simulate the optical network and/or components while you design the physical topology. you can set up traffic, test loss, and threshold values on a section by section basis. you can also model the optical components on your

topology. the following figure shows how you can model a star/tree topology. a serializer is a device that converts data to a serial bit stream. the serializer converts the parallel data stream into a serial data stream. the data rate may be equal to the parallel data rate or less than the parallel data rate. the transmission of the signal of the transmitting part is: the unipolar non return to zero input signal sequence is transformed into a bipolar non return to zero signal sequence through serial/parallel transformation, and then processed into a bipolar non return to zero signal sequence through level transformation. after being output through sampling and root rising cosine filtering, the output qpsk modulation signal is generated by the qpsk modulator and sent to the channel. parameter setting: carrier frequency $f_c = 5$ mhz, bit rate $r = 5$ mbuad, transmit data 50 kbit.

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